



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Steven H. Voldman

Examiner: Unassigned

Serial No.: 10/707,954

Group Art Unit: Unassigned

Filed: January 28, 2004

Docket: BUR920030079US1 (17099)

For: ELECTROSTATIC DISCHARGE INPUT AND
POWER CLAMP CIRCUIT FOR HIGH CUTOFF
FREQUENCY TECHNOLOGY RADIO FREQUENCY
(RF) APPLICATIONS

Dated: March 12, 2004

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with 37 C.F.R. §§ 1.97 and 1.98, it is requested that the one (1) reference, which is listed on the attached Form PTO-1449, be made of record in the above-identified case.

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria VA 22313-1450 on 03/23/04.

Dated: 03/23/04

A handwritten signature that appears to read "C. Mueller".

Inasmuch as this Information Disclosure Statement is being submitted in accordance with the schedule set out in 37 C.F.R. § 1.97(b), no statement or fee is required.

Respectfully submitted,


William C. Roch
Registration No.: 24,972

Scully, Scott, Murphy & Presser
400 Garden City Plaza
Garden City, New York 11530
(516) 742-4343

WCR:jf

<p style="text-align: center;">O I INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)</p> <p style="text-align: center;">MAR 25 2004</p> <p style="text-align: center;">PATENT & TRADEMARK OFFICE</p>				Docket Number (Optional) BUR920030079US1 (17099)		Application Number 10/707, 954		
				Applicant(s) Steven H. Voldman				
				Filing Date January 28, 2004		Group Art Unit		
U.S. PATENT DOCUMENTS								
*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
1								
1								
FOREIGN PATENT DOCUMENTS								
	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)								
		"Silicon Germanium Heterojunction Bipolar Transistor ESD Power Clamps and the Johnson Limit", published Proceedings of EOS/ESD Sympopsis 2002						
EXAMINER				DATE CONSIDERED				
<p>EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</p>								